



Ifw

501.37959CC2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Makoto YOSHIDA et al.
Serial No.: 10/653,889
Filed: September 04, 2003
For: SEMICONDUCTOR INTEGRATED CIRCUIT AND METHOD OF
FABRICATING THE SAME
Art Unit: 2814
Examiner: Pham HOAI

RESPONSE TO RESTRICTION REQUIREMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

June 14, 2004

Sir:

In response to the Restriction Requirement (Paper No. 05212004) dated on May 26, 2004, Applicants elect, without traverse, Group I, Claims 11-13, drawn to a semiconductor device, classified in class 257, subclass 296.

Amendments to the Claims begin on page 2 of this paper.

Remarks/Arguments begin on page 6 of this paper.